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| Patent Number          | 7,213,478                    |
| Issue Date             | May 8, 2007                  |
| Application Number     | 10/776452                    |
| Filing Date            | February 12, 2004            |
| First Named Inventor   | Satoshi Harada               |
| Attorney Docket Number | 071469-0307698 (PC8022A-US1) |

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Assignee of record of the entire interest. See 37 CFR 3.71.  
Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96).



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Signature

*Tugrul Vasar*

Typed or  
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Tugrul Vasar

Date July 28, 2008

Telephone 480-538-2101

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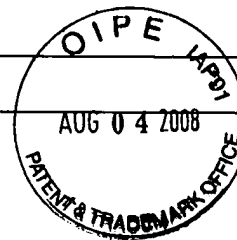


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**STATEMENT UNDER 37 CFR 3.73(b)**Applicant/Patent Owner: Tokyo Electron LimitedApplication No./Patent No.: 7,213,478 Filed/Issue Date: May 8, 2007Entitled: Method for Automatic Configuration of a Processing SystemTokyo Electron Limited, a Corporation

(Name of Assignee)

(Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

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[NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

Tugrul Yasar  
Signature  
Tugrul Yasar  
Printed or Typed Name  
Vice President of Intellectual Property  
Title

July 28, 2008  
Date  
480-539-2101  
Telephone Number

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